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Enhanced Fisica Alonso Acosta
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“Fisica Alonso Acosta” The
expression of Fisica Alonso
Acosta is in our design of high
standards. We do not forget the
details as our style of designing.
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to have more information of the
reputation that we have. The
present invention relates to a
method of selectively removing a
thin film by etching, and more

particularly to a method of selectively removing a thin film by etching, which can selectively remove a thin film, the amount of which to be removed is comparatively large in etching for removing a thin film formed on the surface of a substrate as well as of a thin film which is formed at the surface of a substrate and is formed in a pattern different from that of a thin film to be removed. In a semiconductor device, a multi-layered wiring is formed, and the

resulting wirings are connected to each other by means of a via hole. An example of such a semiconductor device is a semiconductor device for a liquid crystal color display system. In this case, in order to improve the aperture ratio and prevent an electric short circuit, it is necessary to form a via hole which penetrates a plurality of layers of interlayer insulating films. In such a multi-layered structure, an opening is formed in

a relatively thick interlayer insulating film so as to penetrate it, and then, a wiring is formed therethrough, and thereafter, a thin film is formed over the resulting structure. In this case, since it is necessary to use an etching resist which has a high etching resistance, it is necessary to use an organic film having a thickness of about $1\mu\text{m}$ as a protective film. Further, since a liquid crystal substrate is subjected to a high temperature

treatment in an evaporation device of an organic film, it is not desirable to use an inorganic material such as silicon oxide for the organic film. In addition to such a thin film, a thin film for forming electrodes, wiring, and the like is formed in the above multi-layered structure. In the past, in removing such a thin film, a thin film formed on the surface of a semiconductor substrate is etched using an etching liquid, which is prepared by adding a

mixture of an inorganic material
such as silicon oxide, carbon, and

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